

ABSTRACT OF THE DISCLOSURE

A driving circuit for a flat panel display includes a latch unit which is applied a control signal from a shift register to sequentially sample N-bit digital picture signals and to store the picture signals, and simultaneously output the sampled picture signals by a line pass signal; and a voltage to current converting unit distributing an outer reference current to a plurality of paths using a current mirror method, and supplying current of different levels to data lines of the display panel according to logical combinations of the picture signals which are applied from the latch unit.